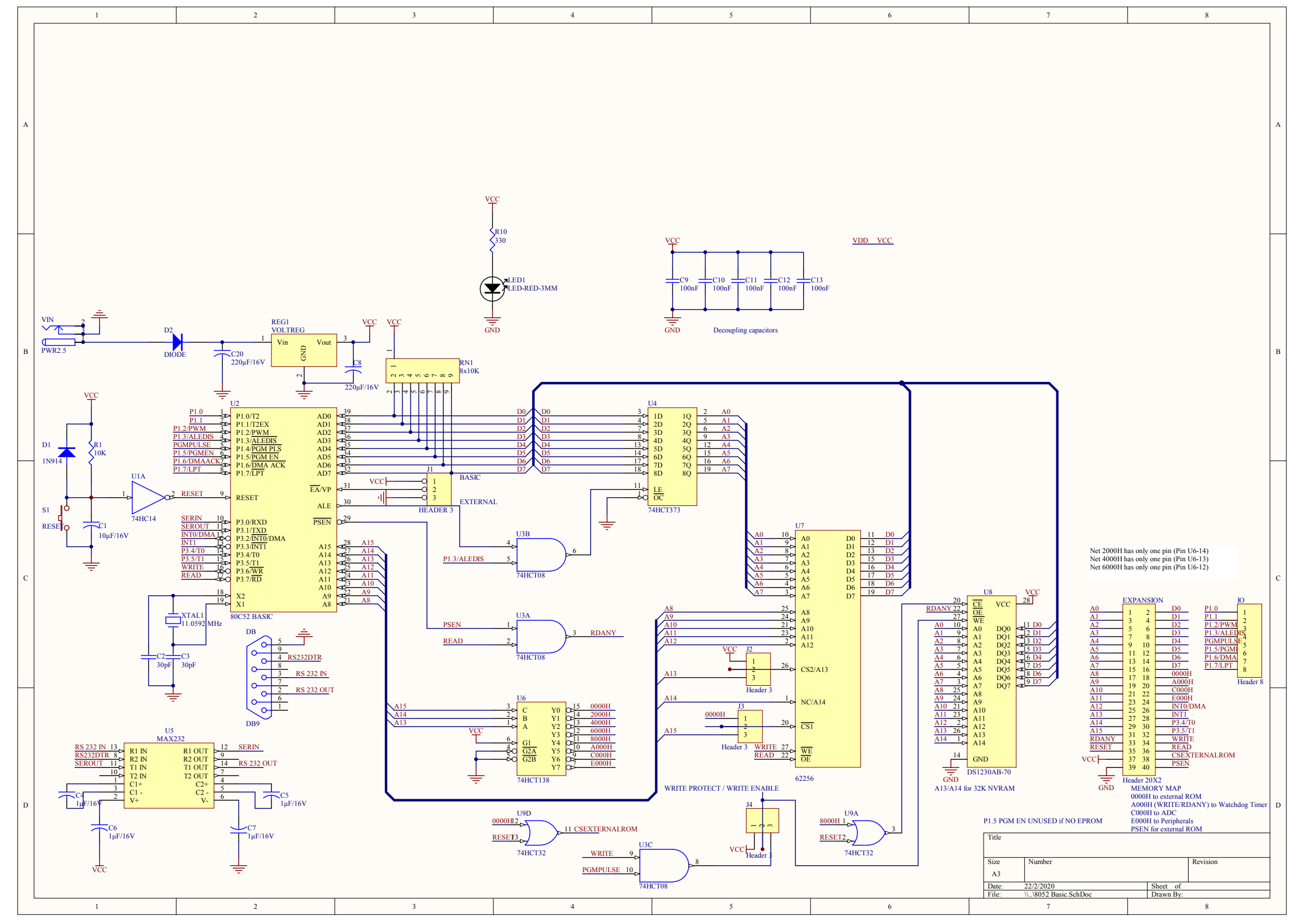
VCC
R10
330
LED1
LED-RED-3MM
GND
VDD VCC
C9 100nF
C10 100nF
C11 100nF
C12 100nF
C13 100nF
Decoupling capacitors
VIN
PWR2.5
D2
DIODE
REG1
VOLTREG
Vin
GND
Vout
C20
220µF/16V
C8
220µF/16V
RN1
8x10K
D1
1N914
R1
10K
S1
RESET
C1
10µF/16V
U1A
74HC14
U2
80C52 BASIC
P1.0/T2
P1.1/T2EX
P1.2/PWM
P1.3/ALEDIS
P1.4/PGM PLS
P1.5/PGM EN
P1.6/DMA ACK
P1.7/LPT
RESET
P3.0/RXD
P3.1/TXD
P3.2/INT0/DMA
P3.3/INT1
P3.4/T0
P3.5/T1
P3.6/WR
P3.7/RD
X2
X1
EA/VP
ALE
PSEN
AD0
AD1
AD2
AD3
AD4
AD5
AD6
AD7
A15
A14
A13
A12
A11
A10
A9
A8
SERIN
SEROUT
INT0/DMA
INT1
P3.4/T0
P3.5/T1
WRITE
READ
XTAL1
11.0592MHz
C2
30pF
C3
30pF
J1
BASIC
EXTERNAL
HEADER 3
U3B
74HCT08
P1.3/ALEDIS
U3A
PSEN
READ
RDANY
U4
74HCT373
LE
OC
U7
62256
CS2/A13
NC/A14
CS1
WE
OE
J2
Header 3
J3
Header 3
0000H
WRITE
READ
U6
74HCT138
0000H
2000H
4000H
6000H
8000H
A000H
C000H
E000H
DB
DB9
RS232DTR
RS 232 IN
RS 232 OUT
U5
MAX232
RS 232 IN
RS232DTR
SEROUT
SERIN
RS 232 OUT
C4
1µF/16V
C5
1µF/16V
C6
1µF/16V
C7
1µF/16V
WRITE PROTECT / WRITE ENABLE
J4
Header 3
U9D
74HCT32
0000H
RESET
CSEXTERNALROM
U3C
74HCT08
WRITE
PGMPULSE
U9A
74HCT32
8000H
RESET
U8
DS1230AB-70
A13/A14 for 32K NVRAM
Net 2000H has only one pin (Pin U6-14)
Net 4000H has only one pin (Pin U6-13)
Net 6000H has only one pin (Pin U6-12)
EXPANSION
IO
Header 8
Header 20X2
CSEXTERNALROM
P1.5 PGM EN UNUSED if NO EPROM
MEMORY MAP
0000H to external ROM
A000H (WRITE/RDANY) to Watchdog Timer
C000H to ADC
E000H to Peripherals
PSEN for external ROM
Title
Size
A3
Number
Revision
Date: 22/2/2020
File: \\..\8052 Basic.SchDoc
Sheet of
Drawn By: